

Abstract of the disclosure

In a shift register circuit included in a driver circuit for driving an active matrix circuit in an active matrix type display device, a plurality of serial-connected registers constructing register lines are arranged to construct a redundancy shift register circuit. Whether or not each register line has defect is examined by detecting an output of a last register of each register line. When the register line having defect is detected by the examination, a normal register line is selected by a shift register selecting switch, and the serial-connected registers of the selected register line are used in the shift register circuit.